


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☒ The ACM Digital Library ☐ The Guide



Searching within **The ACM Digital Library** with **Advanced Search**: (automated and test and development and integrated and circuit and evaluat\* and combined) ([start a new search](#))

Found 25 of 281,018

## REFINE YOUR SEARCH

[Search Results](#) • [Related Journals](#) • [Related SIGs](#) • [Related Conferences](#)

Refine by  
Keywords

Discovered [124,000](#)  
Terms

Refine by People

[Names](#)  
[Institutions](#)  
[Authors](#)  
[Reviewers](#)

Refine by Publications

[Publication Year](#)  
[Publication Names](#)  
[ACM Publications](#)  
[All Publications](#)  
[Publishers](#)

Refine by Conferences

[Sponsors](#)  
[Events](#)  
[Proceeding Series](#)

Results 1 - 20 of 25

 Sort by  in 

 Save results to a  
Binder

Result page: 1 2 next &gt;&gt;

### 1 [A simple mechanism for capturing and replaying wireless channels](#)

Glenn Judd, Peter Steenkiste

 August **E-WIND '05**: Proceedings of the 2005 ACM SIGCOMM workshop on Experimental approaches to 2005 wireless network design and analysis

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (6.06 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)
**Bibliometrics:** Downloads (6 Weeks): 38, Downloads (12 Months): 410, Downloads (Overall): 2285, Citation Count: 3

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility ...

**Keywords:** channel capture, emulation, wireless

### 2 [Architecture description language \(ADL\)-driven software toolkit generation for architectural exploration of programmable SOCs](#)

Prabhai Mishra, Aviral Shrivastava, Nikil Dutt

 July **DAC '04**: Proceedings of the 41st annual Design Automation Conference 2006

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (1.07 MB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)
**Bibliometrics:** Downloads (6 Weeks): 20, Downloads (12 Months): 144, Downloads (Overall): 831, Citation Count: 2

Advances in semiconductor technology permit increasingly complex applications to be realized using programmable systems-on-chips (SOCs). Furthermore, shrinking time-to-market demands, coupled with the need for product versioning through software modification ...

**Keywords:** Architecture description language, design space exploration, embedded processor, programmable architecture, retargetable compilation

Also published in:

 July 2006 **Transactions on Design Automation of Electronic Systems (TODAES)** Volume 11 Issue 3

## ADVANCED SEARCH

[Advanced Search](#)

## FEEDBACK

[Please provide us with feedback](#)

Found 25 of 281,018